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a multiply/accumulate device for multiplying said received signal received on said receive input by said predetermined CDMA code word and operating in the analog domain, said multiply/accumulate device operable to accumulate the results of the multiplication operation over a symbol period to provide an analog result; and
a data conversion device for determining if the analog result corresponds to a predetermined digital state and, if so, generating a digital output corresponding to said predetermined digital state;

[The CDMA decoder of Claim 1,] wherein said multiply/accumulation device comprises:

at least one series leg including first and second series connected transistors disposed between a first node and a second reference voltage, said first transistor having the gate thereof connected to the received signal on said receive input and the gate of said second transistor connected to receive said associated code word;

a storage device connected to said first node;

a precharge device for enabling said first node to be precharged to a defined level prior to the initiation of the accumulation operation at the beginning of a symbol period;
and

wherein said first and second transistors provide a multiplication operation for said code word and the received signal and said storage device is operable to accumulate the results of the multiplication operation over a symbol period.

REMARKS

Allowable claims 14 and 15 are now written in independent form. Claims 1 to 21 remain active in this application.